

FIG. 1
PRIOR ART

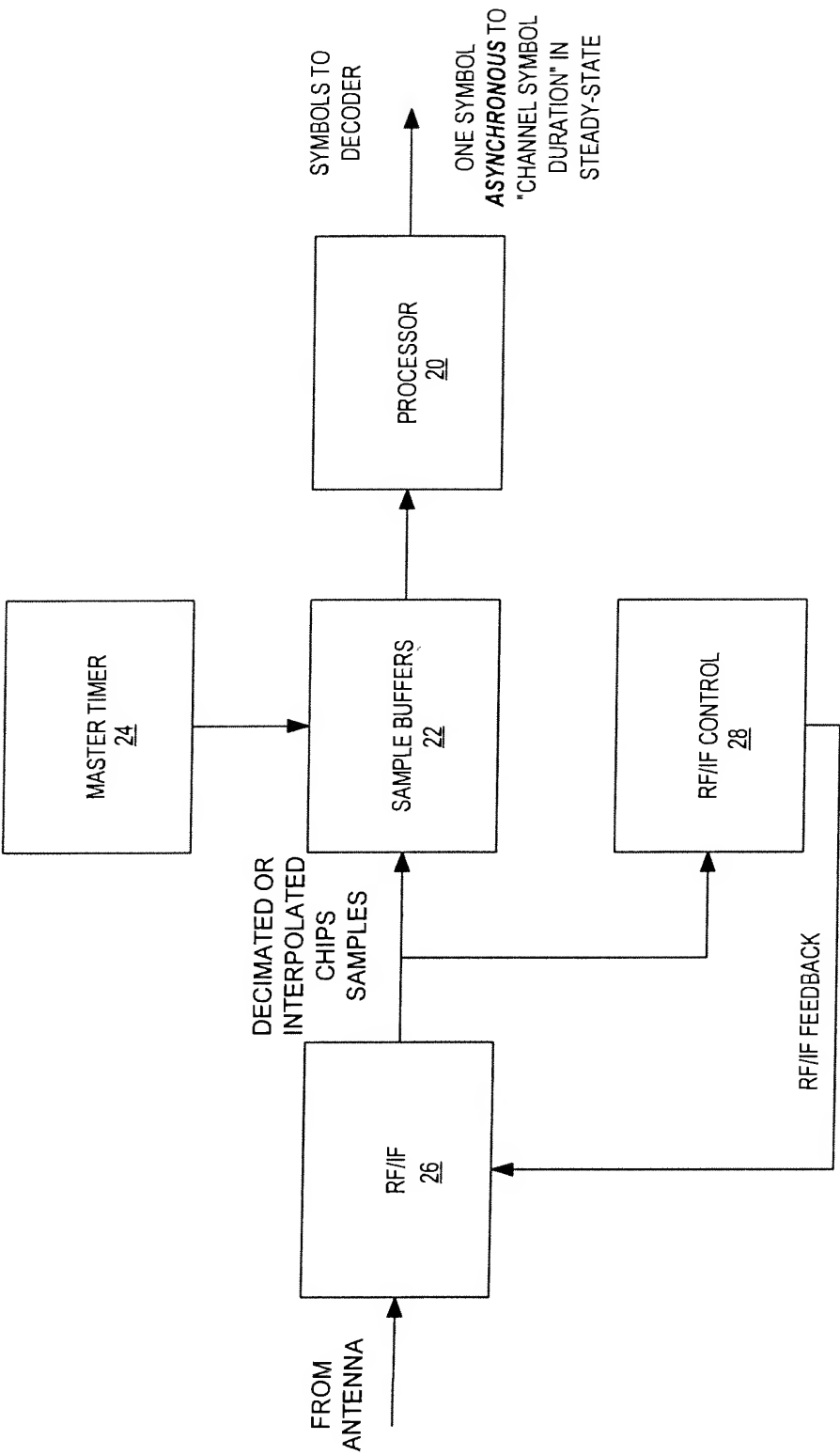
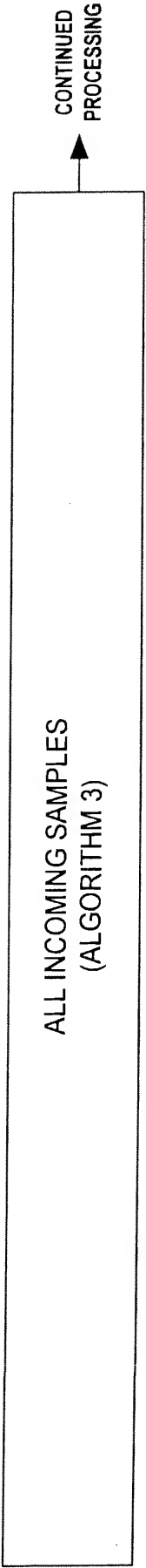


FIG. 2

CONVENTIONAL PROCESSING



OPERATION OF PROCESSOR 20

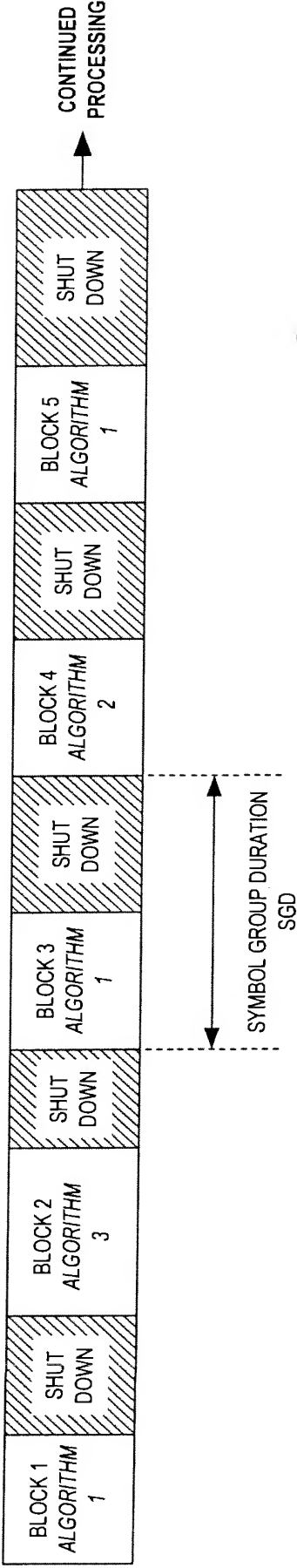


FIG. 3

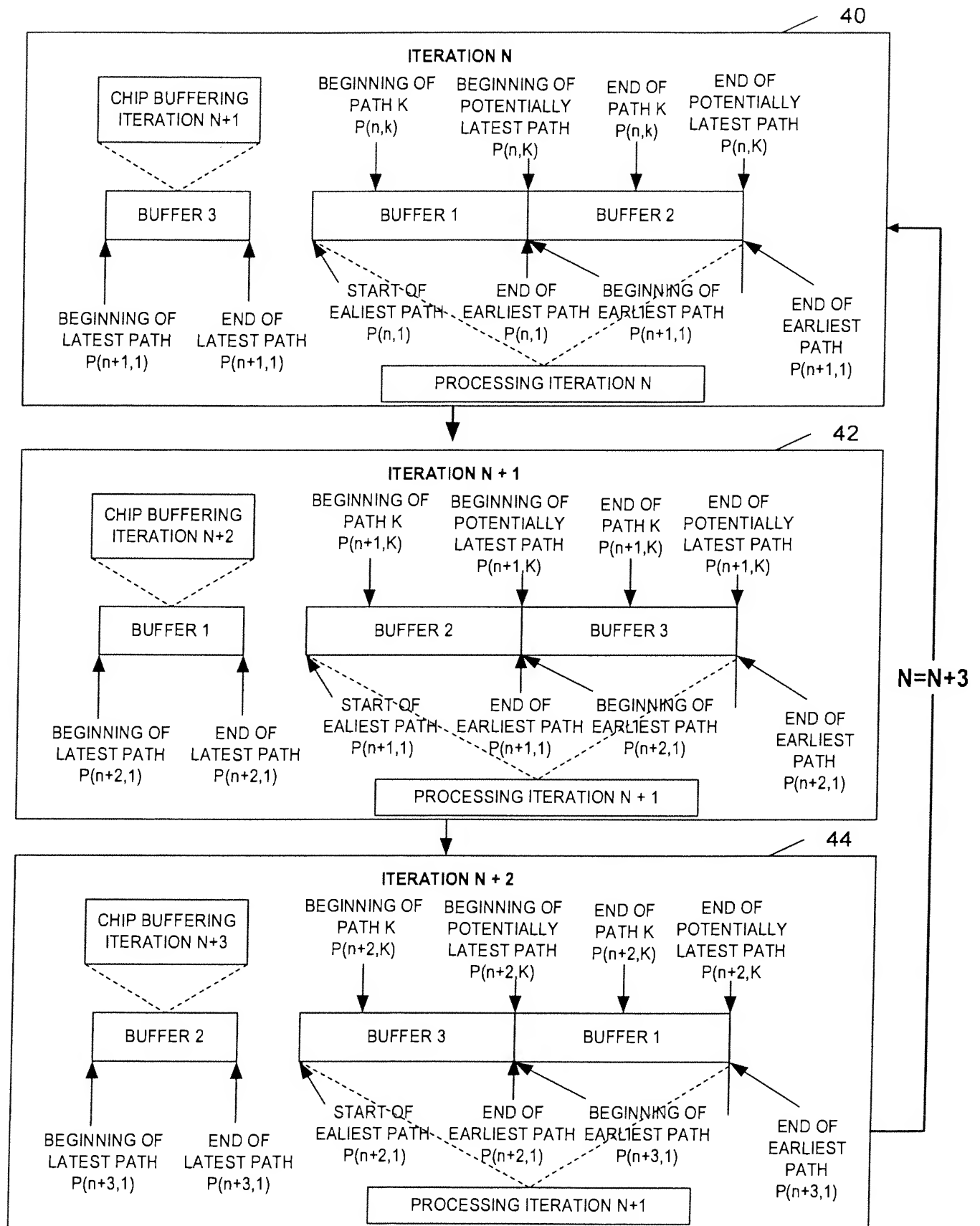


FIG. 4

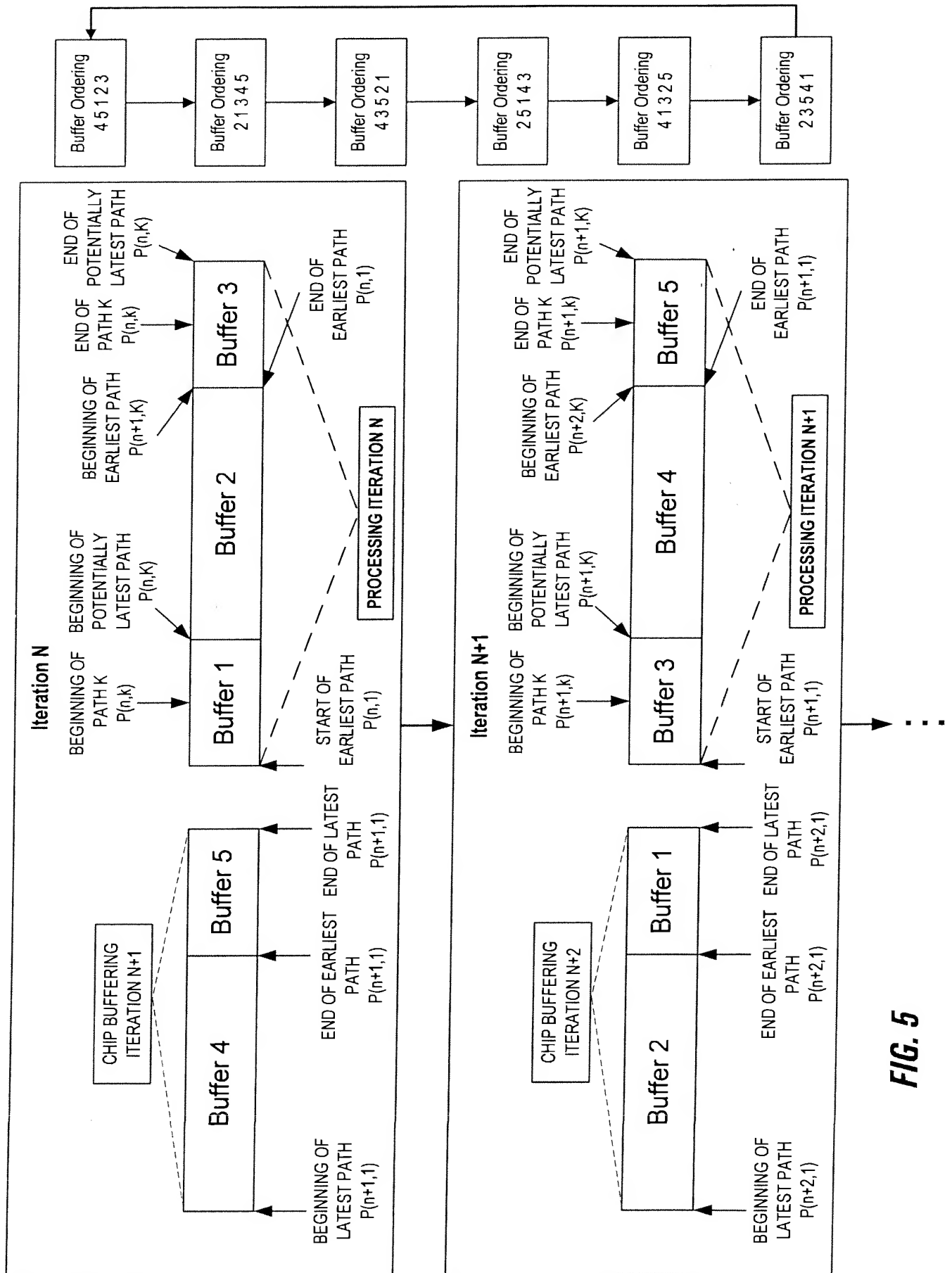


FIG. 5

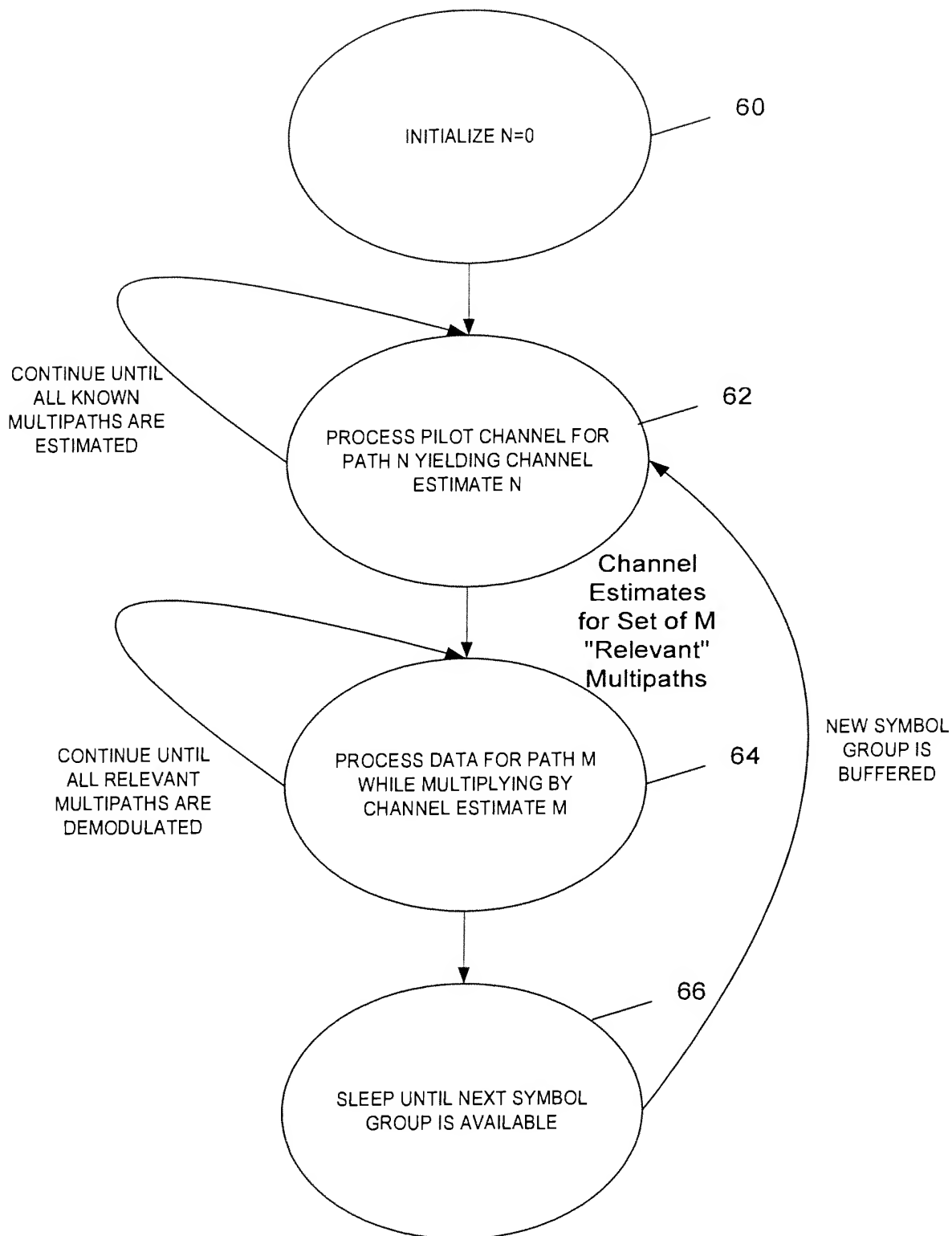
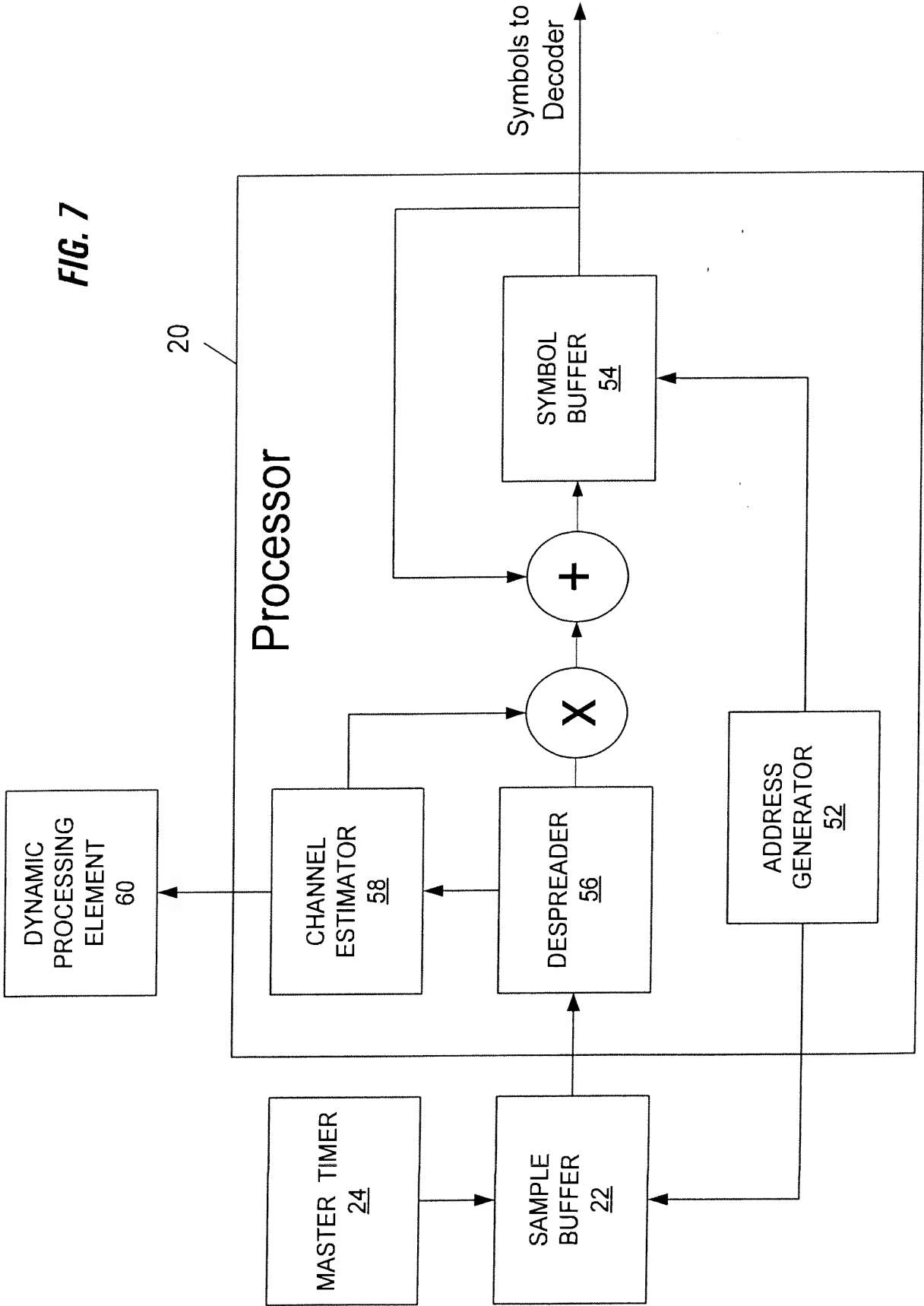


FIG. 6



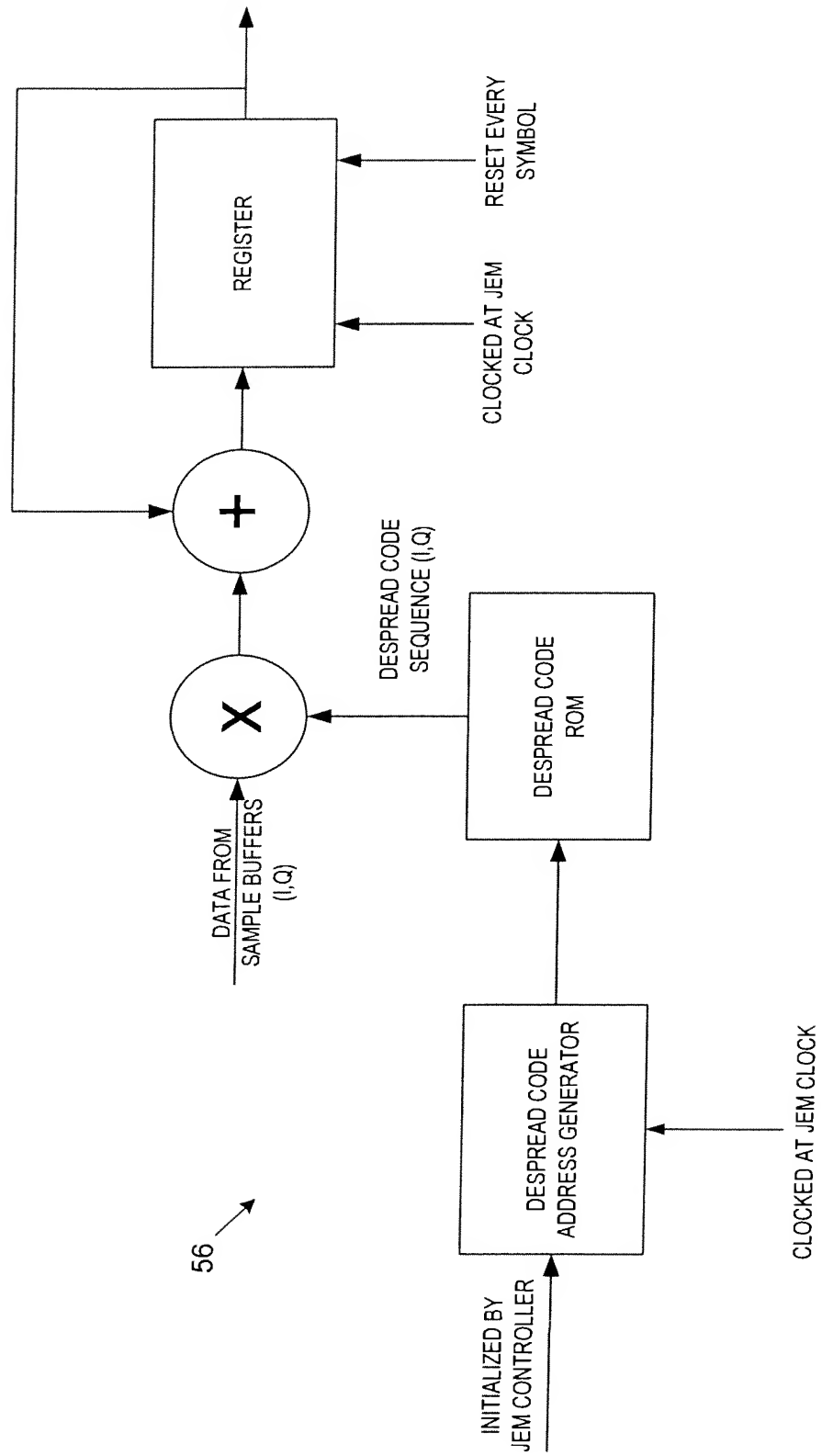


FIG. 8

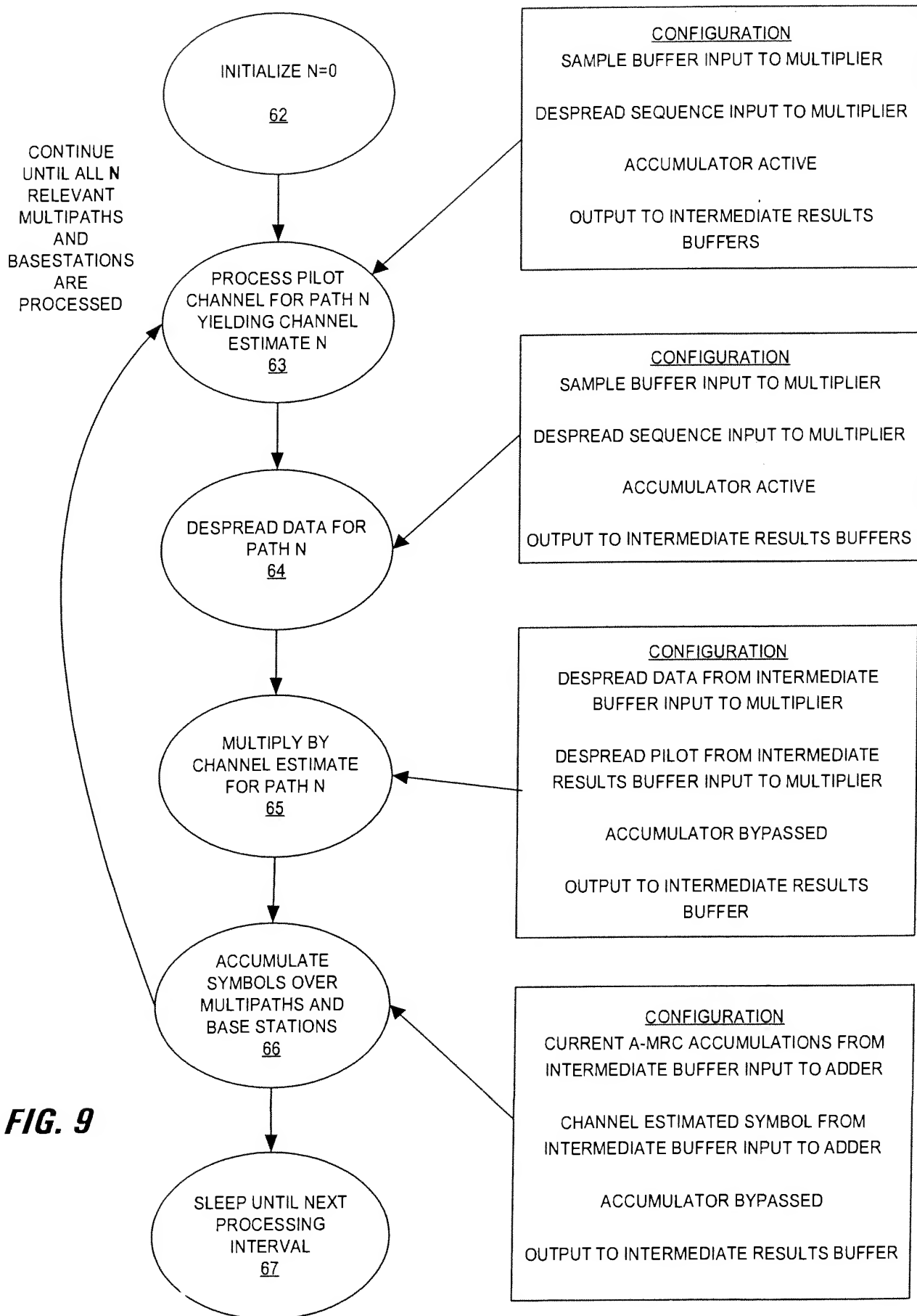


FIG. 10

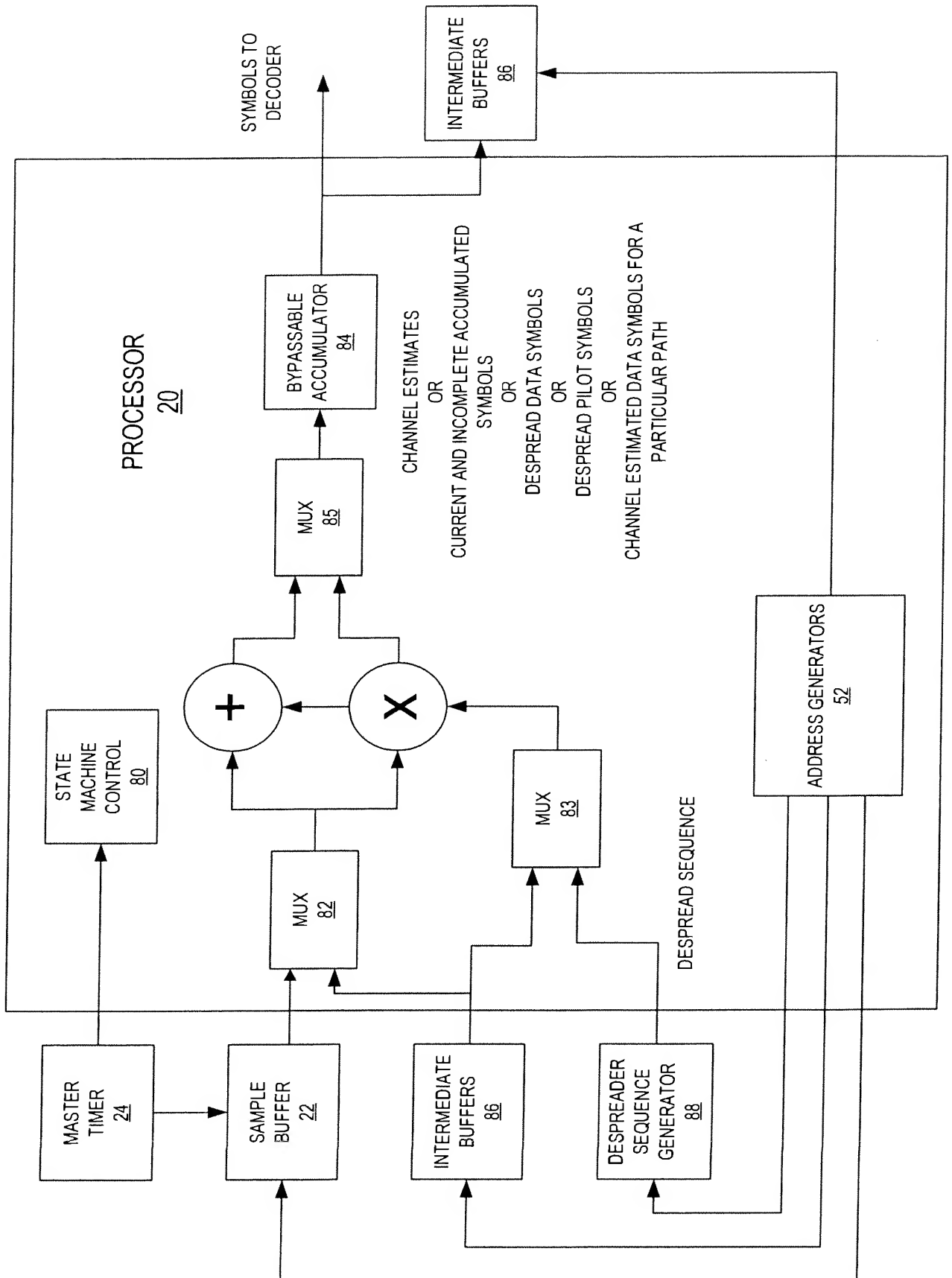


FIG. 11
PRIOR ART

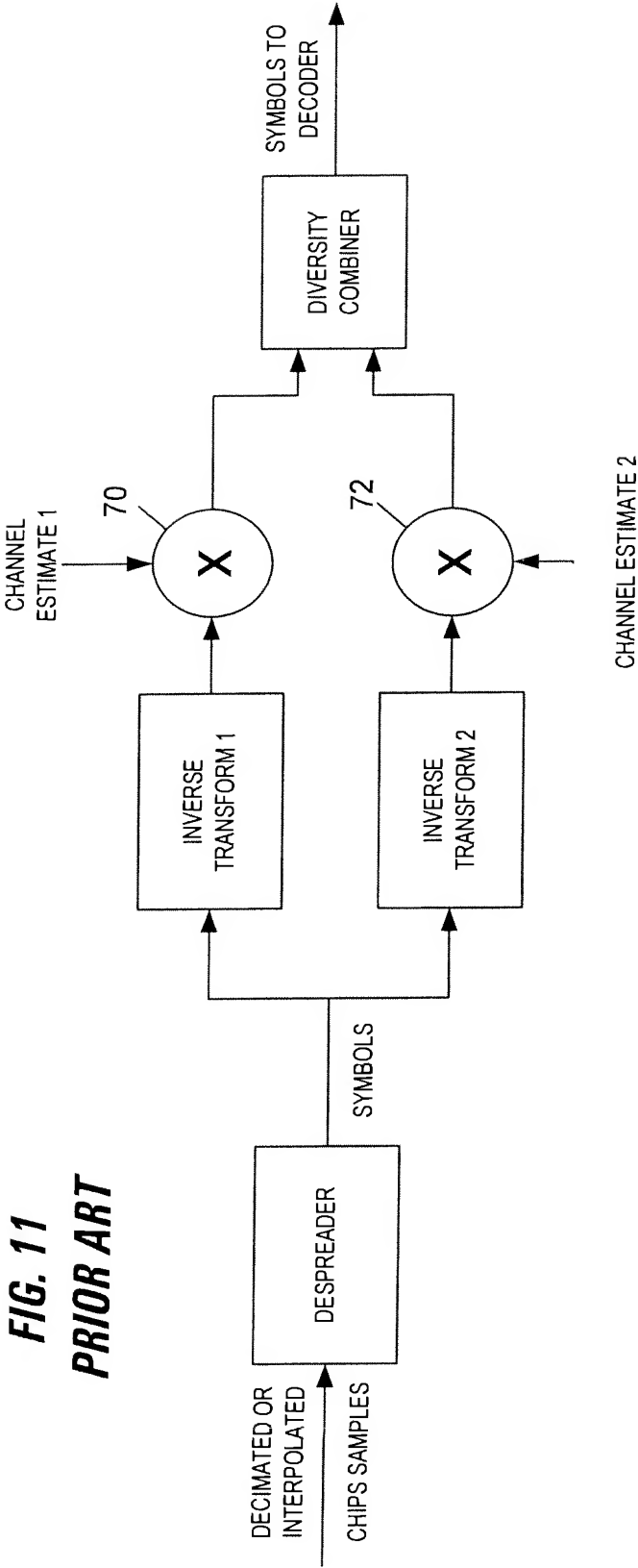
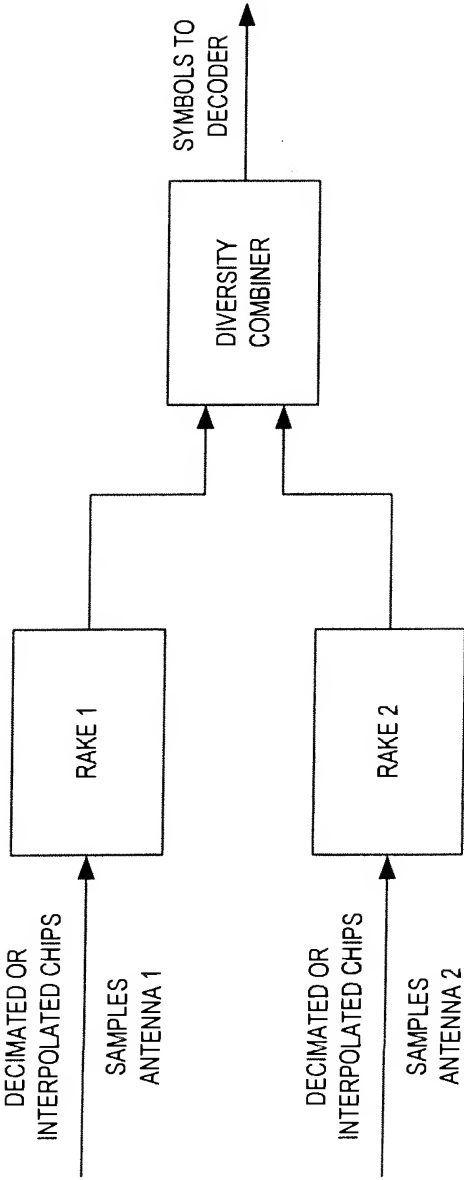


FIG. 12
PRIOR ART



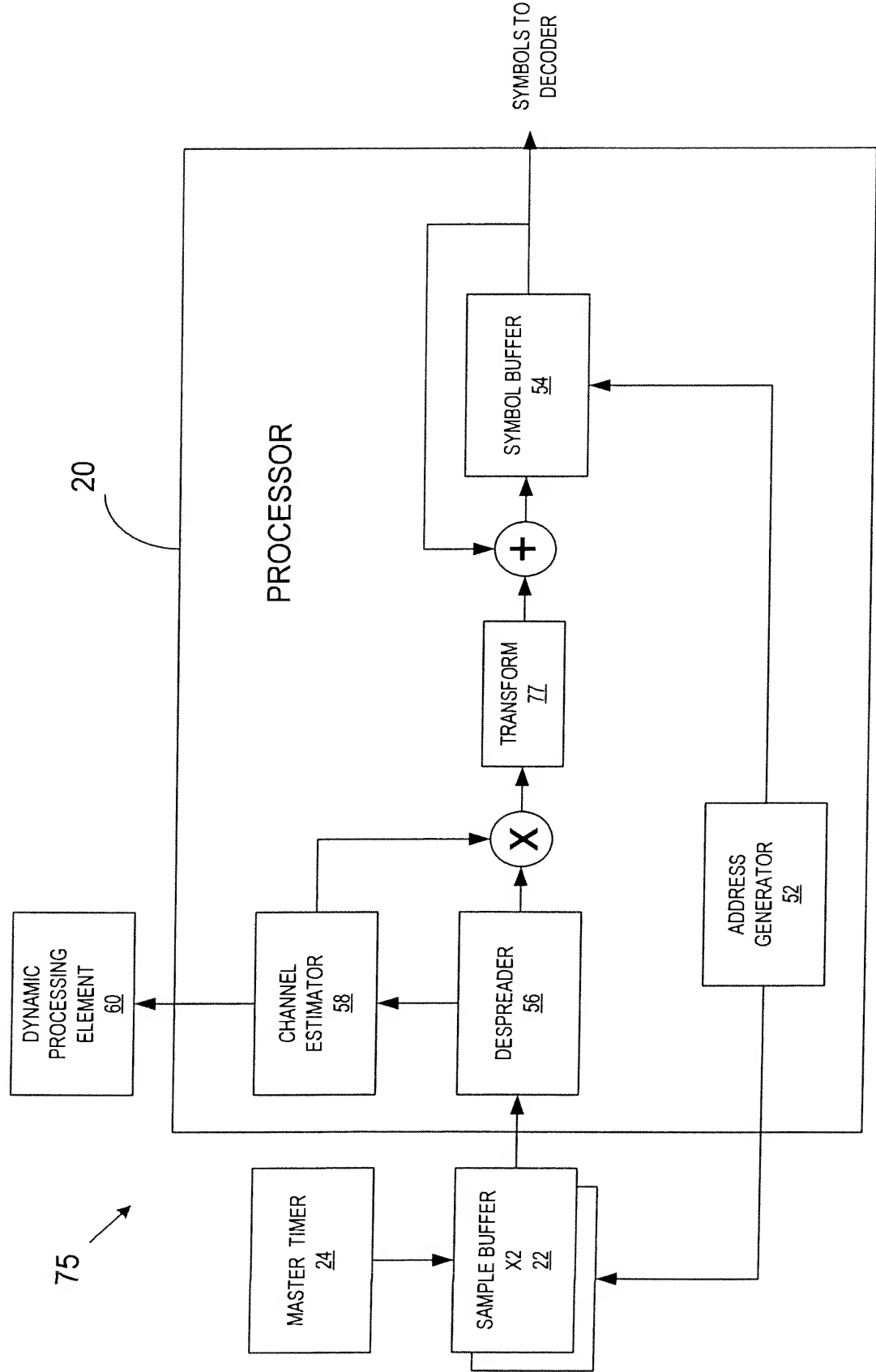


FIG. 13

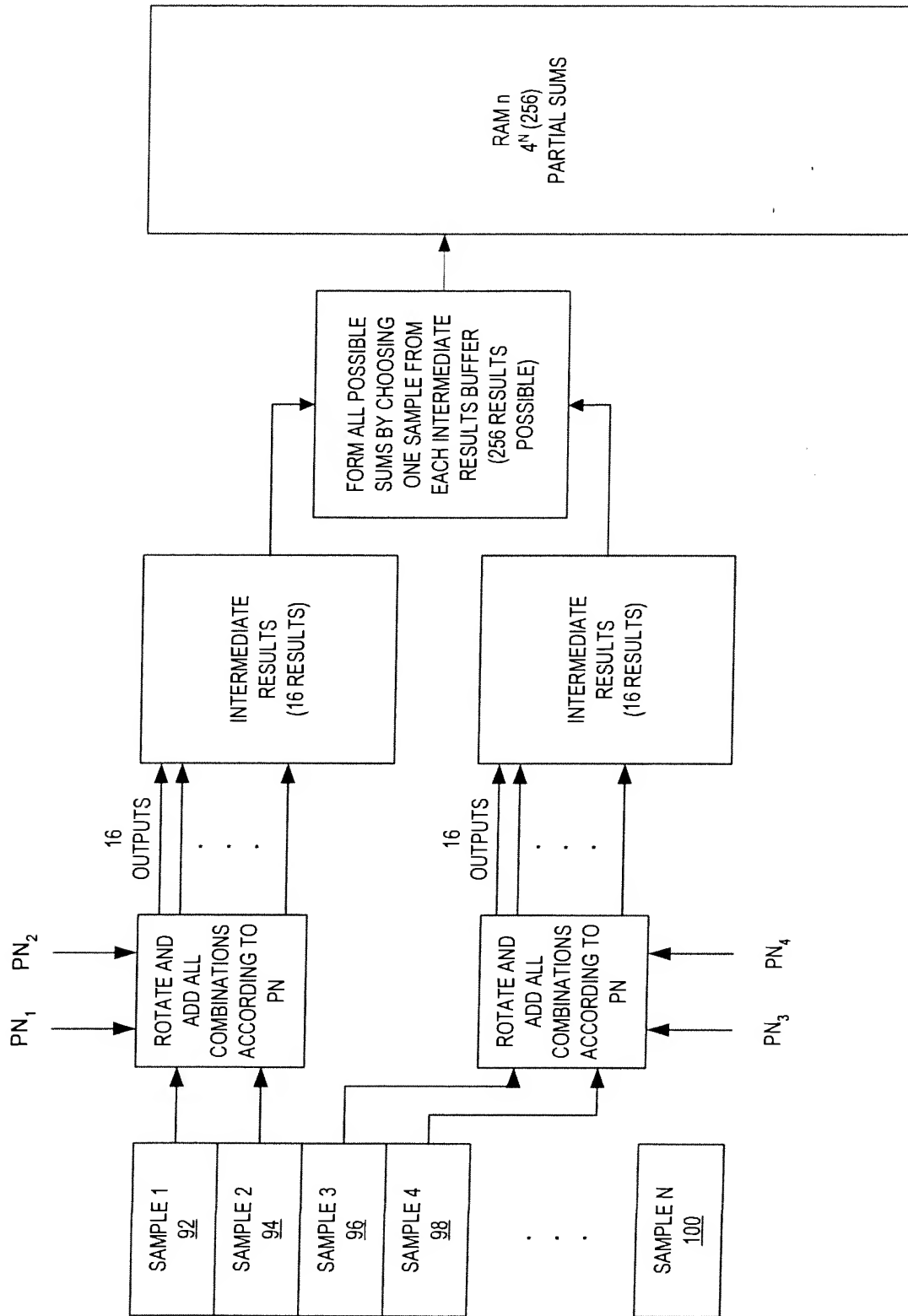


FIG. 14

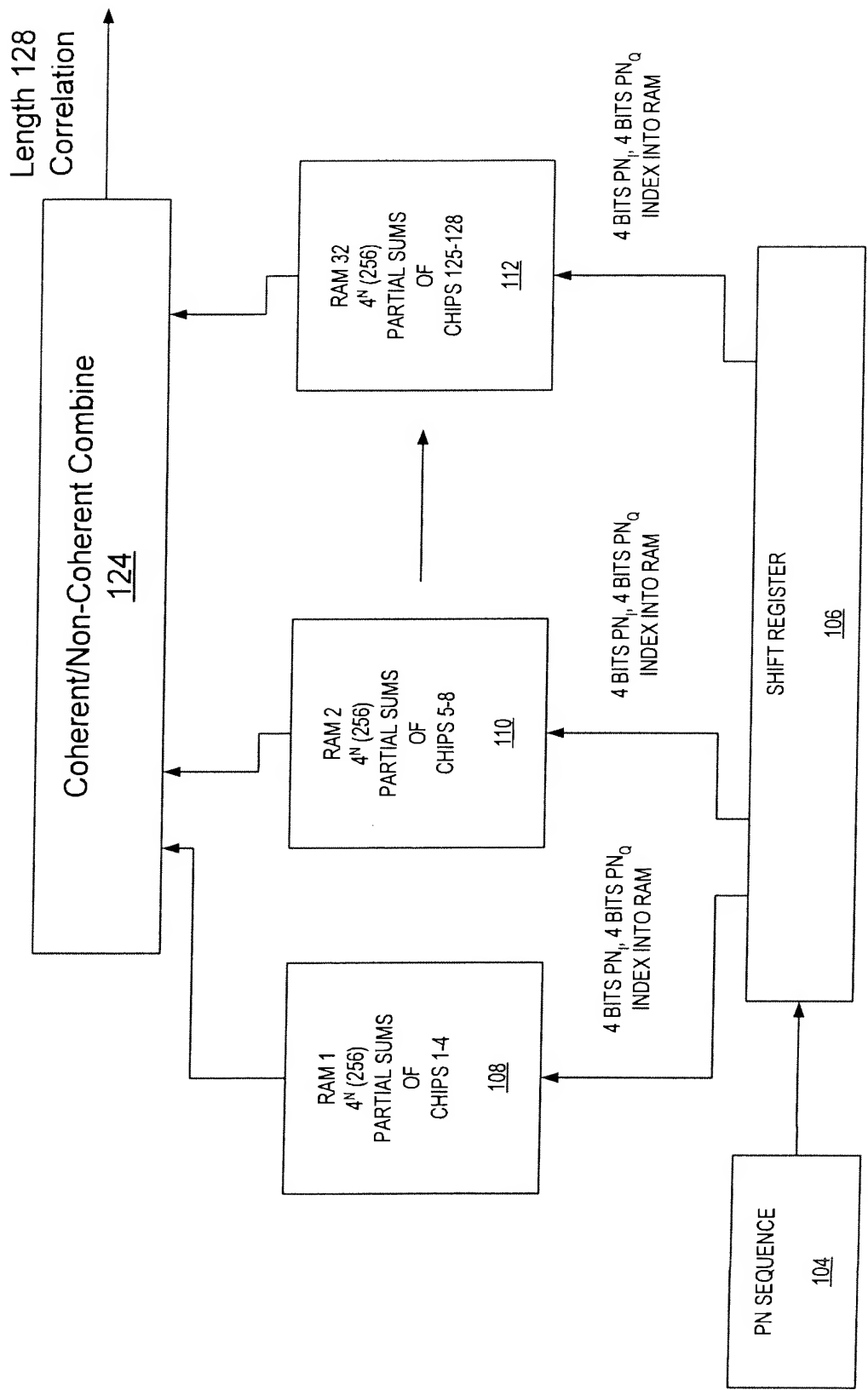


FIG. 15

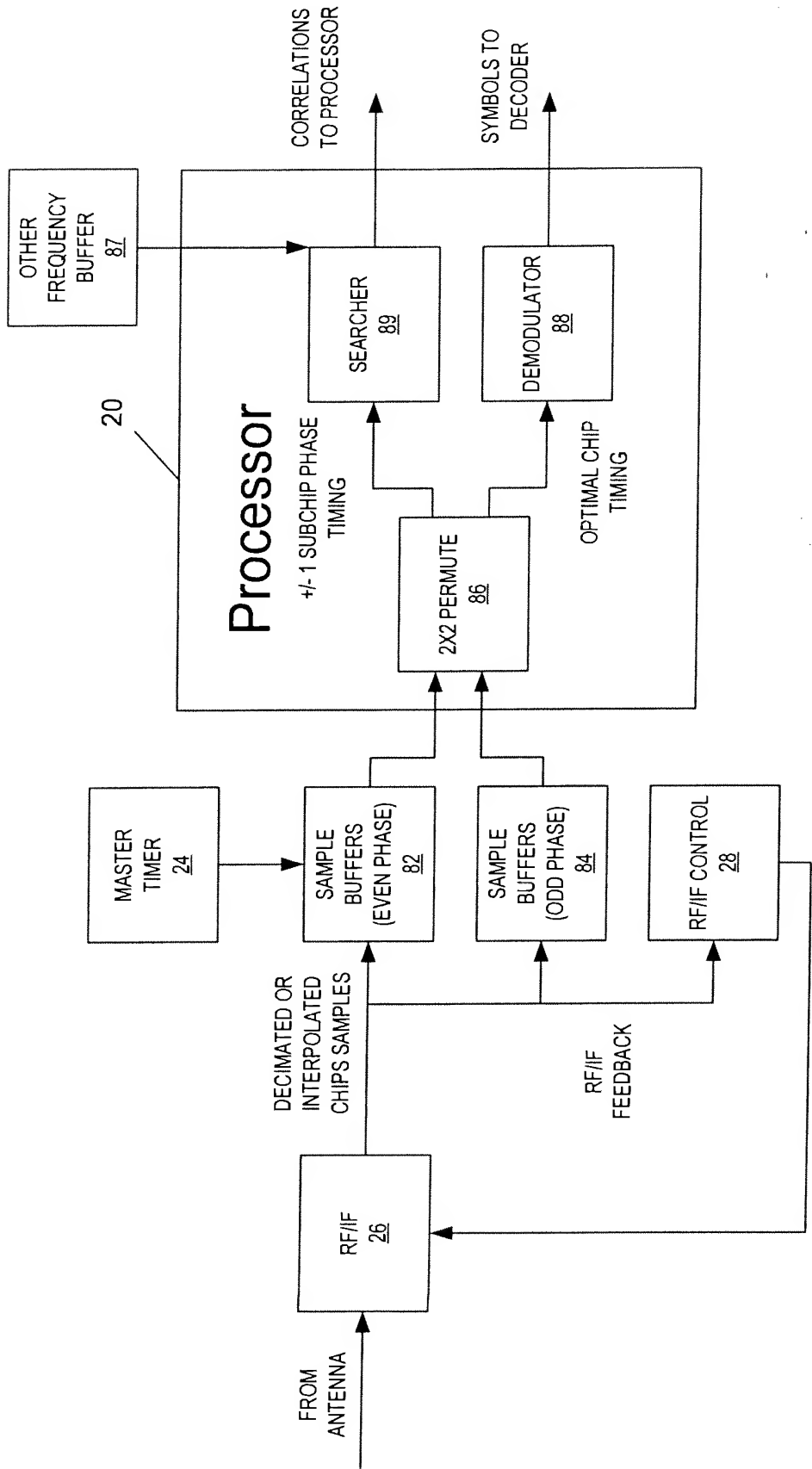


FIG. 16

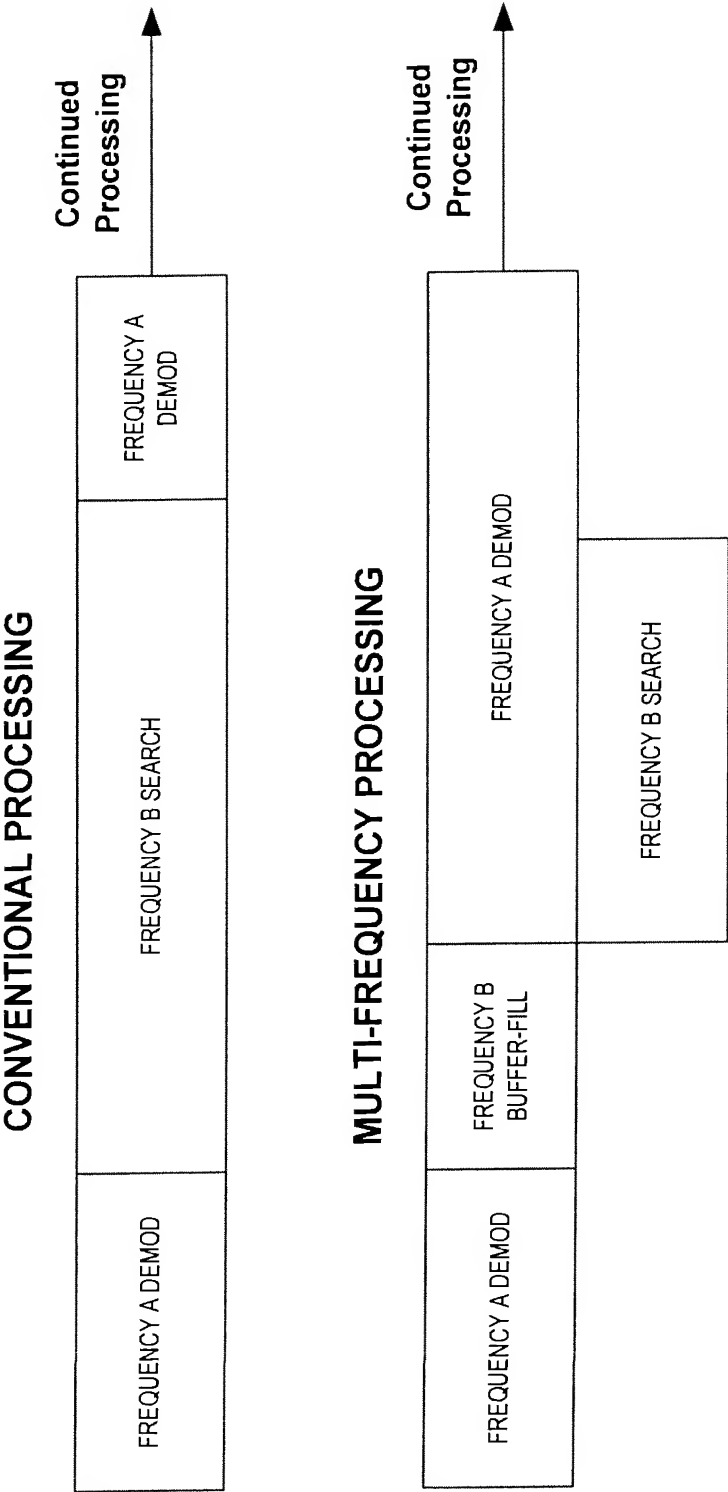


FIG. 17

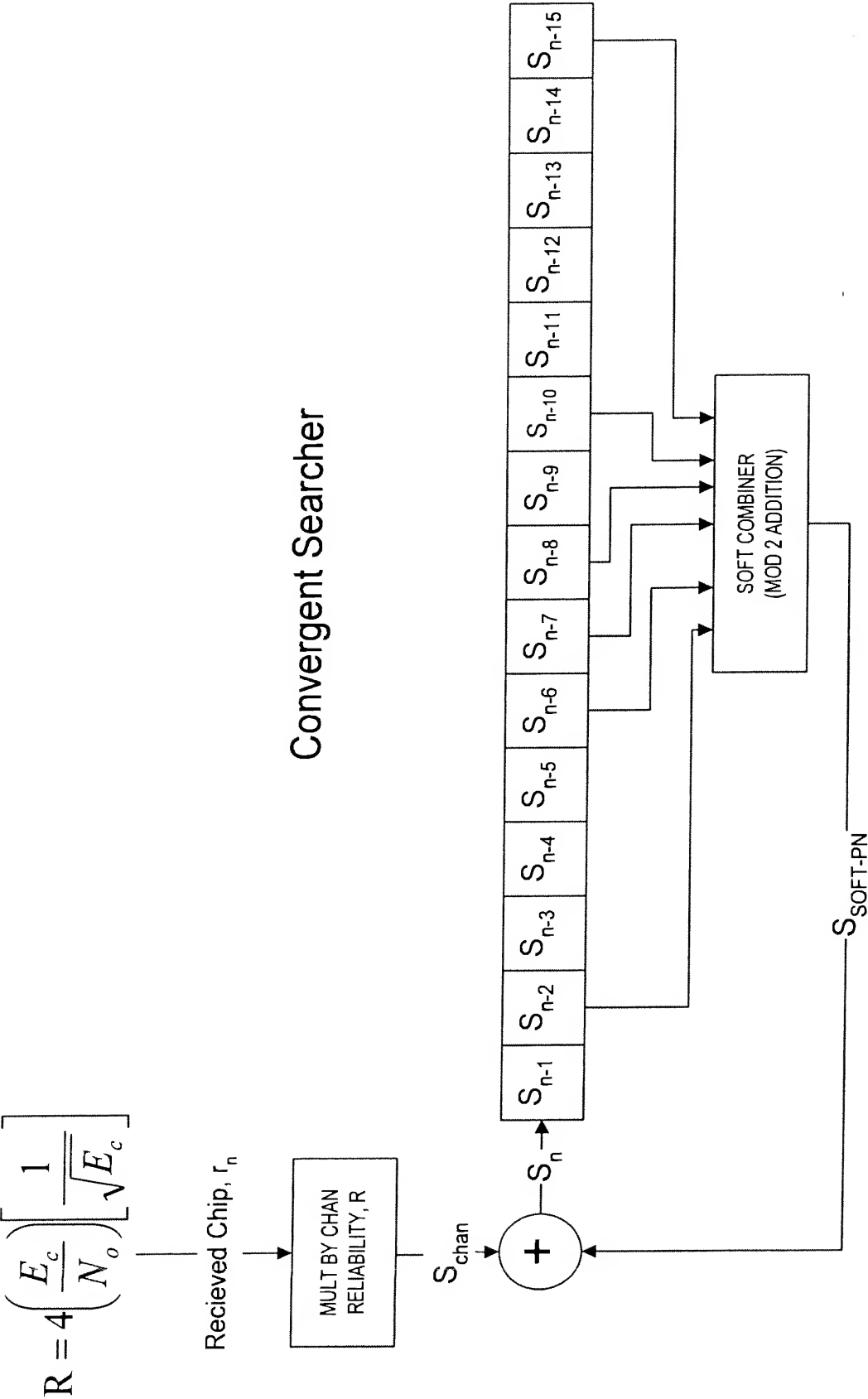


FIG. 18

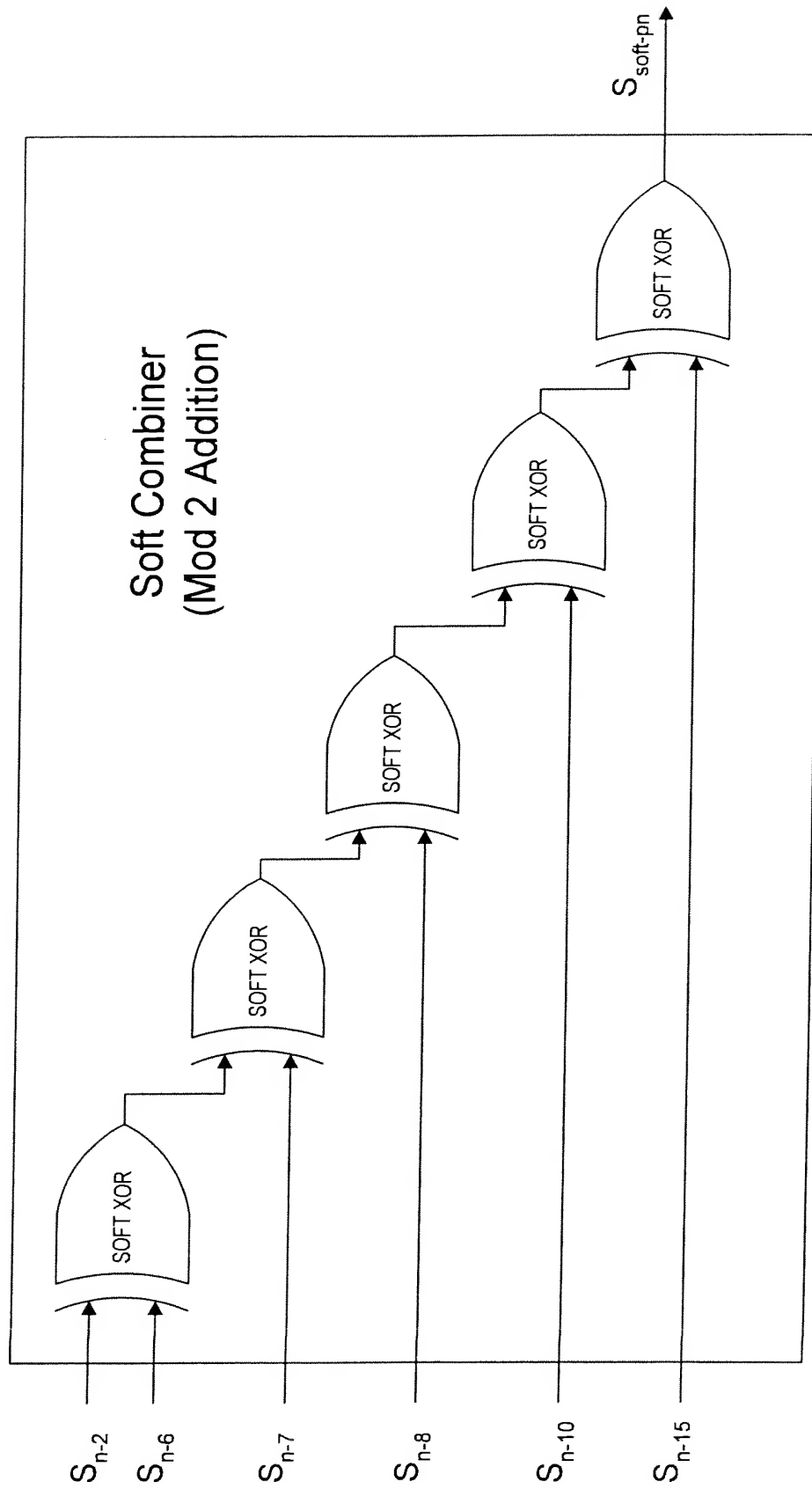


FIG. 19

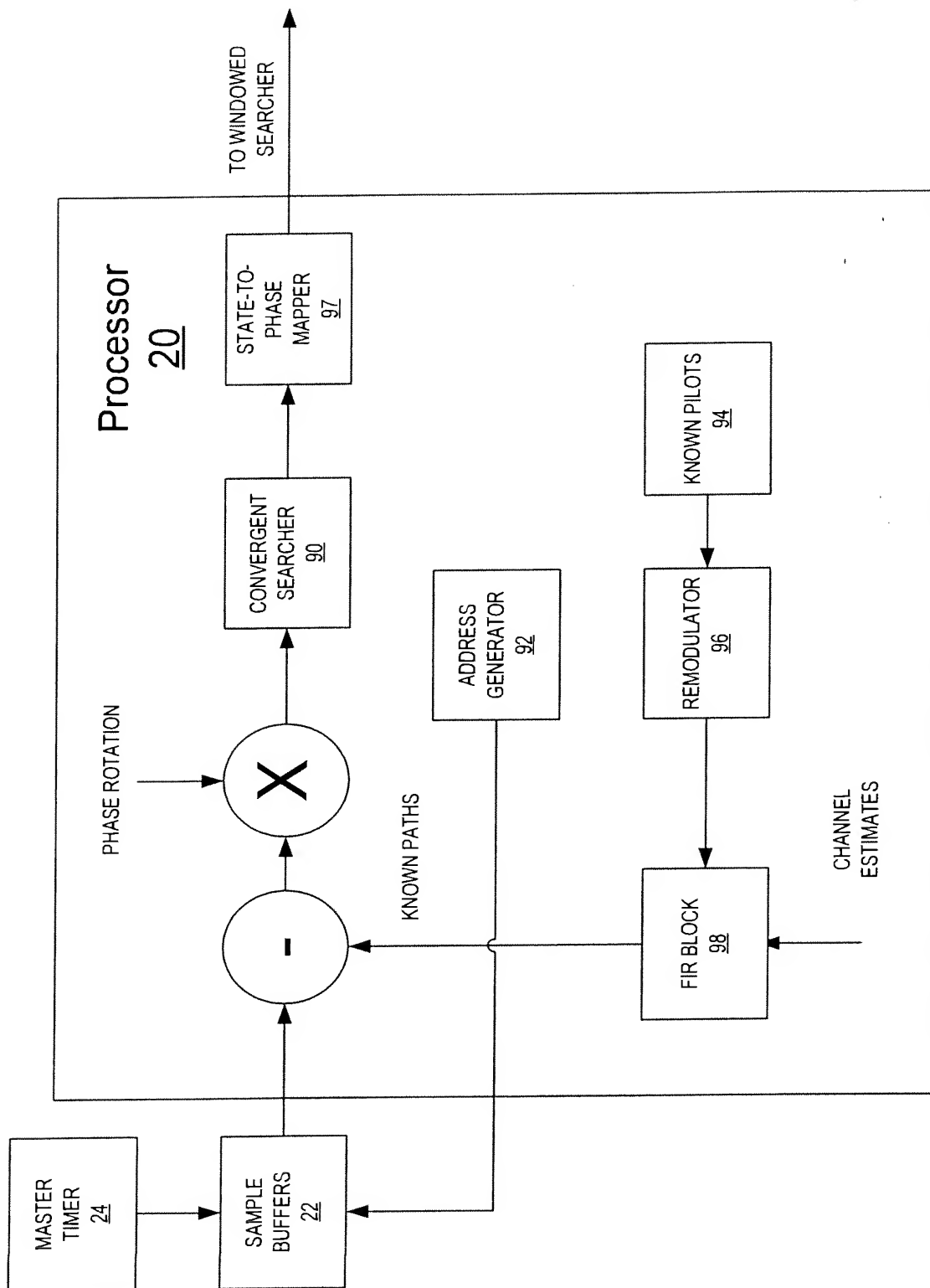


FIG. 20

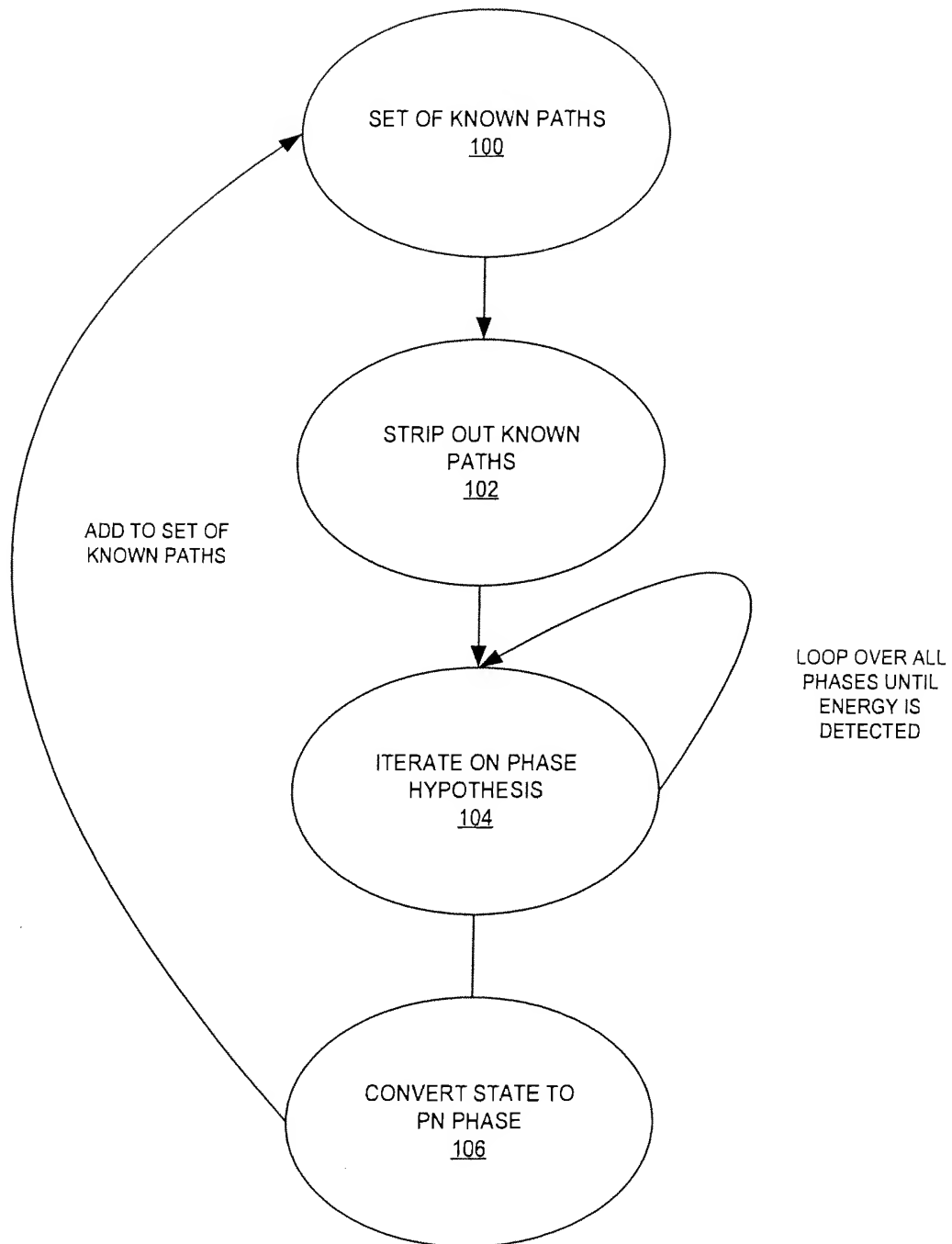


FIG. 21